

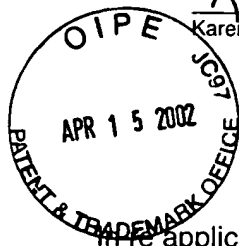
Serial No. 10/016,449

2133
#2/4-2-02
PATENT
IBM Docket No. RPS920010127US1

I hereby certify that this correspondence (37 CFR 1.97(b)) and attached documents (i.e., PTO 1449 and copies of all cited references) are being deposited with the United States Postal Service as first class mail in an envelope with sufficient postage addressed to: Assistant Commissioner for Patents, Washington, DC 20231.

Karen Orzechowski
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APRIL 11, 2002
Date of Deposit



IN THE UNITED STATES PATENT & TRADEMARK OFFICE

the application of
R. T. Bailis, et al.

Serial No. 10/016,449

Filed: Dec. 10, 2001

: April 8, 2002
: IBM Corporation - 9CCA/B002
: P.O. Box 12195
: Research Triangle Park,
: North Carolina 27709
: Unit: 2133

For: METHOD AND SYSTEM FOR USE OF A FIELD PROGRAMMABLE GATE ARRAY (FPGA)
FUNCTION WITHIN AN APPLICATION SPECIFIC INTEGRATED CIRCUIT (ASIC) TO
ENABLE CREATION OF A DEBUGGER CLIENT WITHIN THE ASIC

**TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
WITHIN THREE MONTHS OF FILING OR BEFORE MAILING
OF FIRST OFFICE ACTION (37 CFR 1.97(b))**

Assistant Commissioner for Patents
Washington, DC 20231
Sir:

The information disclosure statement transmitted herewith is being filed within three months of the filing date of the application or date of entry into the national stage of an international application or before the mailing date of a first Office action on the merits, whichever occurs last, under 37 CFR 1.97(b); no fee is required.

This Information Disclosure Statement is being submitted in connection with the above-identified application for patent. Applicants submit herewith patents, publications or other information of which they are aware, which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 C.F.R. § 1.56.

While this Information Disclosure Statement may be "material" pursuant to 37 C.F.R. § 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 C.F.R. § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information

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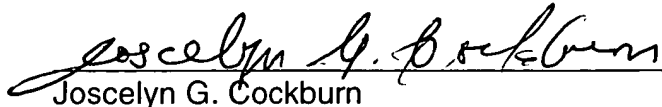
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as defined in 37 C.F.R. § 1.56(a) exists.

The attached form, PTO-1449, provides a listing of patents, publications, or other information as required by 37 C.F.R. § 1.98(a)(1).

A copy of each of the items identified on the attached Form PTO-1449 is supplied herewith.

Respectfully submitted,



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